MODEL 7500SD PARTIAL INSERTION MAGSTRIPE READER/ENCODER

Description

Precision manual insertion and withdrawal modules for both reading and encoding on a single cycle (e.g., read on insertion and encode on withdrawal). Takes standard credit card-size cards and badges with dual magnetic stripes. Designed for direct mounting into OEM systems. Available with one or two data tracks on each magnetic stripe at ANSI/ISO Track #1 and #2 locations. Track #3 location is reserved for timing track used in encoding. Reads and encodes a nominal 2-3/8 inches of card length. Real time or buffered serial data and strobe output on read pass. Requires serial data and strobe input on encode pass. Card detect sensors at entry and/or at rear of module.

Applications

Debit-Use Cards

- Copy Machines
- Dispensing Machines and Stations

Record-Use Cards

- High Security Access
- Limited Use Passes
- Club and Resort ID
- Off-Line Data Collection

Encoding Cards

- Proprietary Card Issuance
- Card Renewal
- Integrated Credit/Debit Systems

Features

Interface

- Strobe Data

Read Mode

- Read on insertion or withdrawal or both
- Timing track not required for reading

Encode Mode

- Encode on insertion or withdrawal
- Reliable encoding over broad range of speed and acceleration
- Auto-verification of encoded data
- Serial data and strobe input

Additional Features

- Current   40 ma READ, 130 ma ENCODE (Typ)
- Weight   7-_ oz (206 gms)
- No motor or transport mechanism to jam
- Encodes dual stripe cards with timing track
- Reads single or dual stripe cards
- Dimensions: 3/10"W x 1.185"H x 3.55"L (w/o Bezel)
- Reads and encodes all ISO cards
- Partial Stripe Insertion reader/encoder
- UL Compliant

Input/Output Signals

ENCODE MODE SIGNALS   TTL/CMOS Compatible

WRITE ENABLE   Input. Low activates the encode
                            Drivers.

WRITE CLOCK   Output: One clock pulse for each data bit
                            encoded. Gated by DATA PRESENT signal.

WRITE DATA   Input. Data for encoding, high for ONE-bit, low
                            for ZERO-bit, to be presented in response to
                            WRITE CLOCK.

TIME TRACK   Output. The 2F signal from the timing track.

Standard Body Configurations

- Model 750x—Read/Encode data tracks at left
- Model 751x—Read/Encode data tracks at right

Track Selection

x Digit	Read Tracks	Encode Tracks
1	Track #1	Track #1
2	Track #2	Track #2

Pinouts for 7500SD

1	- Ext. Reset
2	+5 VDC
3	0 VDC
4	0 VDC
5	- Data Present
6	- F. R. Detect
7	- Card Detect (Front)
8	- Buffer Loaded*
9	- Read Buffer (* - Serial Data)
10	+ Read Data (* + Serial Data)
11	– Read Strobe (* - Data Strobe)
12	+ Read Strobe (* - Host Ready)
13	Host Ready (*)
14	Decode/ - Buffer Mode Select
15	Write Data
16	– Write Clock
17	– Write Enable
18	Time Track
19	– Card Detect (Rear)
20	+5 VDC

*When in Buffer Mode